



Department of ECE
Summary Sheet of VALUE ADD COURSES
ACADEMIC YEAR 2018-19

S. No	Name of the works hop/ Seminar/ Conference	Type of the Event	Resource Person Details	Number Of Participants	Course No	Date From-To
1	PCB DESIGN AND ITS APPLICATIONS	VALUE ADDED COURSE	Mr. S. SAI SANDEEP	99	2018-19/PEC/ECE/IVYE AR/VAC001	11-02-2019 to 16-02-2019
2	MIXED SIGNAL DESIGN	VALUE ADDED COURSE	Dr. Aswin kumar SV	90	2018-19/PEC/ECE/IVYE AR/VAC002	15-02-2019 to 20-02-2019
3	CAD FOR VLSI	VALUE ADDED COURSE	Dr. V. Venkata Reddy	90	2018-19/PEC/ECE/IVYE AR/VAC003	11-03-2019 to 16-03-2019



O.V. Road, KANDUKUR - 523 105.
PRAKASAM (Dist.), AP. INDIA.

T : 08598 222288, 221200, F : 08598 221300
E : pec@prakasamec.com
W : www.prakasamec.com



Department of ECE
BROCHERS of VALUE ADDED COURSES conducted in Academic year 2019-20

BROCHER

PCB DESIGN AND ITS APPLICATIONS 11-02-2019 to 16-02-2019

<p>CHIEF PATRON Dr. K. Ramaiah Secretary & Correspondant Prakasam Engineering College Kandukur</p> <p>PATRON Dr. M. Lakshmanarao M.Tech., Ph.D. Principal Prakasam Engineering College Kandukur</p> <p>CONVENER Dr. CH. Ravi Kumar M.Tech., Ph.D. Head of Department Department of Electronics and Communication Engineering Prakasam Engineering College, Kandukur</p> <p>Co-Ordinator Mr. I. Ramakoteswara Rao M.Tech. Associate Professor</p>	<p>PRAKASAM ENGINEERING COLLEGE Approved by AICTE, New Delhi Affiliated to JNTU-Kakinada</p> <p>NAAC PRAKASAM ENGINEERING COLLEGE</p> <p>CAD for VLSI VAC003- CAD FOR VLSI 11.03.2019-16.03.2019 Organized by Electronics and Communication Engineering</p> <p>venue: PEC 1/5/SEMINAR-2 HALL</p> <p>For any details, please contact Course in-charge Mr. I. Ramakoteswara Rao M.Tech. Associate Professor 9848622648</p>
--	---

<p>COURSE OBJECTIVES</p> <p>The main objective of the course is to expose the students to different web technologies and prepare them to design, develop and maintain a web site</p> <p>COURSE OUTCOMES (COs)</p> <p>At the end of the course, Students can able to</p> <ol style="list-style-type: none">Understand the practical situations where mixed signal analysis is required.Analyze and handle the inter-conversions between signals.Design systems involving mixed signals.Explain various VLSI design flows, design methods and technologiesDescribe various VLSI design steps and relevant design automation tools, Explain types of synthesis, Illustrate design representations and graph based problem formulations.Illustrate and apply CAD algorithms used in VLSI design automation. <p>EMINENT SPEAKER</p> <p>Mr. K. Hanumantha Rao HOD of ECE, Professor Prakasam Engineering College Kandukur</p> <p>Accredited by NAAC Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by MHRD Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p>	<p>CONTENT</p> <ul style="list-style-type: none">➤ I Introduction to VLSI Design Methodologies: The VLSI design problem, Design domains, Design Actions, Design methods and technologies➤ II Review of VLSI Design Automation Tools: Quick tour of design automation tools for various methods and levels of VLSI design,➤ Physical Design, Verification, Design Management➤ III High Level Synthesis: Introduction to Synthesis, Design representations and transformations➤ IV High Level Synthesis Algorithms: Partitioning, Scheduling, Allocation algorithms➤ Background images, colors and properties, manipulating texts, using fonts➤ V Floor Planning and Placement: Floor planning concepts, Shape Functions and Floor plan sizing, Placement,➤ Types of placement problems and algorithms➤ VI Routing: Local routing, Types of local routing problems,➤ Area and Channel routing➤ problems and algorithms, Global routing➤ Design rules, symbolic layout, Algorithms for layout <p>Accredited by NAAC Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by MHRD Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p> <p>Accredited by BCI Approved by AICTE, New Delhi Affiliated to JNTU - Kakinada</p>
--	--



O.V. Road, KANDUKUR - 523 105.
PRAKASAM (Dist.), AP. INDIA.
T : 08598 222288, 221200, F : 08598 221300

E : pec@prakasamec.com
W : www.prakasamec.com

O.V. Road, KANDUKUR - 523 105.
PRAKASAM (Dist.), AP. INDIA.
T : 08598 222288, 221200, F : 08598 221300

E : pec@prakasamec.com
W : www.prakasamec.com

BROCHER

MIXED SIGNAL DESIGN 15-02-2019 to 20-02-2019

CHIEF PATRON

Dr.K.Ramaiah

Secretary & correspondant
Prakasam Engineering college Kandukur

PATRON

Dr.M.Lakshmanarao M.Tech.,Ph.D.
Principal

Prakasam Engineering college Kandukur

CONVENER

Co-Ordinator

Mr.G.Suresh M.Tech.

Associate Professor
Head of Department
Department of Electronics and Communication Engineering
Prakasam Engineering college,Kandukur



PRAKASAM

ENGINEERING COLLEGE

Approved by AICTE, New Delhi | Affiliated to JNTU-Kakinada

Mixed Singal Desing

SN

VAC001 - Mixed Singal Desing

15.02.2019-20.02.2019

Organized by

Electronics and Communication Engineering



venue:PEC 1/5/SEMINAR-2 HALL

For any deals,please contact

Course in-charge
Mr.G.Suresh M.Tech.
Associate Professor
9985191866

COURSE OBJECTIVES

To introduce how to handle the practical situations where mixed signal analysis is required.

COURSE OUTCOMES (COS)

At the end of the course, student will be able to

1. Understand the practical situations where mixed signal analysis is required.
2. Analyze and handle the inter-conversions between signals.
3. Design systems involving mixed signals.
4. Understand the practical situations where mixed signal analysis is required.
5. Analyze and handle the inter-conversions between signals.

EMENANT SPEAKER

Mr.Aswin Kumar SV
HOD OF ECE & Associate Professor
KLEF DEEMED TO BE UNIVERSITY

CONTENT

- Analog and discrete-time signal processing, introduction to sampling theory; Analog continuous-time filters:
- passive and active filters. Basics of analog discrete-time filters and Z-transform.
- Switched-capacitor filters- Non idealities in switched-capacitor filters
- At the end of the course, Students can able to
- Switched-capacitor filters- Non idealities in switched-capacitor filters
- capacitor filter architectures
- Basics of data converters;
- Successive approximation ADCs, Dual slope ADCs,
- Pipeline ADCs, Hybrid ADC structures, High-resolution ADCs, DACs
- Mixed-signal layout, Interconnects and data transmission
- Borders and boxes, margins, padding lists, positioning using CSS
- data transmission, Current-mode signaling and data transmission
- Voltage-mode signaling
- Introduction to frequency synthesizers
- synchronization,
- Digital PLLs, DLLs.
- Basics of PLL,



ESTABLISHED
2001



2001

O.V. Road, KANDUKUR - 523 105.
PRAKASAM (Dist), AP, INDIA.
T: 08598 222288, 221200, F: 08598 221300
E: peci@prakasamec.com
W: www.prakasamec.com

Approved by AICTE, New Delhi | Affiliated to JNTU - Kakinada



2001

O.V. Road, KANDUKUR - 523 105.
PRAKASAM (Dist), AP, INDIA.
T: 08598 222288, 221200, F: 08598 221300
E: peci@prakasamec.com
W: www.prakasamec.com

Approved by AICTE, New Delhi | Affiliated to JNTU - Kakinada



BROCHER

CAD FOR VLSI11-03-2019 to 16-03-2019

<p>CHIEF PATRON Dr.K.Ramaiah Secretary & correspondant Prakasam Engineering college Kandukur</p> <p>PATRON Dr.M.Lakshmanarao M.Tech.,Ph.D. Principal Prakasam Engineering college Kandukur</p> <p>CONVENER Dr.CH.Ravi Kumar M.Tech.,Ph.D. Head of Department Department of Electronics and Communication Engineering Prakasam Engineering college,Kandukur</p> <p>Co-Ordinator Mr.G.Suresh M.Tech. Associate Professor</p>	<p>PRAKASAM ENGINEERING COLLEGE Approved by AICTE,New Delhi Affiliated to JNTU-Kakinada</p> <p>PCB Design VAC001 - PCB Design 11.02.2019-16.02.2019 Organized by Electronics and Communication Engineering</p> <p>venue:PEC 1/5/SEMINAR-2 HALL</p> <p>For any deals,please contact Course in-charge Mr.G.Suresh M.Tech. Associate Professor 9985191866</p>
<p>COURSE OBJECTIVES</p> <p>This course will teach teams of students how to design and fabricate PCB for prototyping as well as in Industrial Production environment.</p> <p>COURSE OUTCOMES (COS)</p> <p>At the end of the course, student will be able to</p> <ol style="list-style-type: none">1. Establishing infrastructure and simulations2. Concept of programming the in WSN environment3. The students will be able to4. Understand a single layer and multilayer PCB5. Create and fabricate a PCB6. Evaluate and test a PCB <p>EMENANT SPEAKER</p> <p>Mr.S.SAI SANDEEP VISHISHTA INNOVATORS PVT</p> <p>PROJECT MANAGER,GUNTUR</p>	
<p>CONTENT</p> <ul style="list-style-type: none">► Need for PCB, Types of PCBs : Single and Multilayer, Technology: Plated Through Hole, Surface Mount, PCB Material► Electronic Component packaging, PCB Designing, Fabrication, Production.► Proprietary tools like Eagle, Ultiboard, Orcad and OpensourceAt the end of the course, Students can able to► Transmission line► Forms► Cross talk and Thermal management► Introduction to KiCad, Schematic entry / drawing,► layering, component foot print library► design rules, component placing: Manual & automatic, track routing► track length, angle, joint & size,► Autorouter setup► IPC standards for schematics► material and documentation► CNC Machine, Photo-Lithography process► PCB Mass Manufacturing Process: Gerber Generation,► CAM, panelization, cleaning, drilling,► automated optical inspection,	

